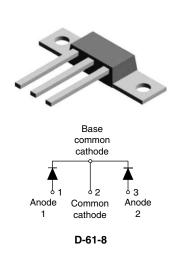


Vishay High Power Products

Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A



PRODUCT SUMMARY			
I _{F(AV)}	2 x 40 A		
V _R 80/100 V			

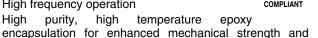
FEATURES

High

- 175 °C T_{.1} operation
- · Center tap module
- Low forward voltage drop

moisture resistance

High frequency operation



- · Guard ring for enhanced ruggedness and long term reliability
- · New fully transfer-mold low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Lead (Pb)-free
- · Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	80	А	
V _{RRM}	Range	80/100	V	
I _{FSM}	t _p = 5 μs sine	7000	A	
V _F	40 Apk, T _J = 125 °C (per leg)	0.67	V	
T _J	Range	- 55 to 175	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	83CNQ080APbF	83CNQ100APbF	UNITS	
Maximum DC reverse voltage	V_{R}	80	100	V	
Maximum working peak reverse voltage	V_{RWM}	60	100	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 132 °C, rectangular waveform		80	
Maximum peak one cycle	on-repetitive surge current per leg I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	7000	Α
See fig. 7		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	720	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1 \text{A}, L = 30 \text{mH}$		15	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

83CNQ...APbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	40 A	T _{.I} = 25 °C	0.81	V
Maximum forward voltage drop per leg		80 A	1j=25 C	1.00	
See fig. 1		40 A	- T _J = 125 °C	0.67	
3		80 A		0.82	
Maximum reverse leakage current per leg	r leg I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	1.5	- mA
See fig. 2		T _J = 125 °C		35	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1400	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance,	per leg	В	DC operation See fig. 4	0.85	
junction to case	per package	R_{thJC}	DC operation	0.42	°C/W
Typical thermal resistance, case to heatsink (D-61-8 only)		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	3711
Approximate weight				7.8	g
Approximate weight				0.28	OZ.
Mounting torque	minimum	Recommended hardware 3M stainless screw		12 (10)	kgf ⋅ cm
Mounting torque	maximum		necommended nardware SW Stamless Screw	24 (20)	(lbf \cdot in)
Marking device			Case style D-61	83CN0	Q100A

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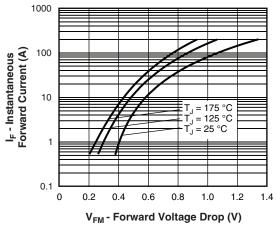


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

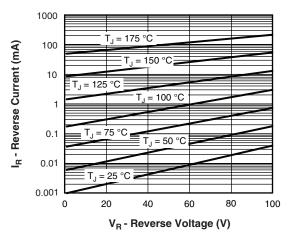


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

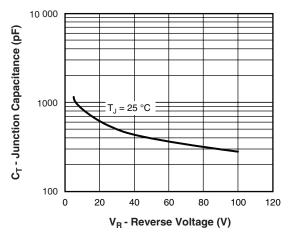


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

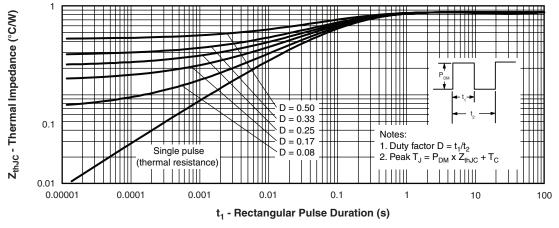


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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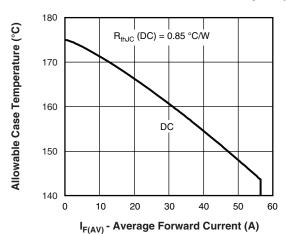


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

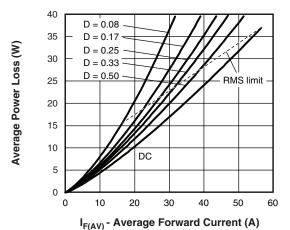


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

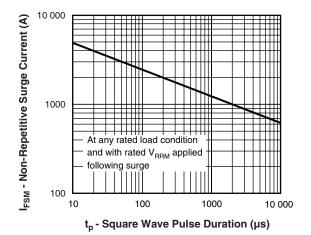


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

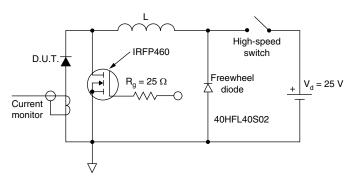


Fig. 8 - Unclamped Inductive Test Circuit

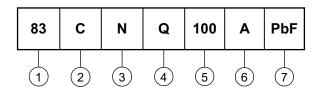


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ORDERING INFORMATION TABLE

Device code



1 - Current rating (80 A)

2 - Circuit configuration:

C = Common cathode

3 - Package:

N = D-61

- Schottky "Q" series

080 = 80 V 100 = 100 V

Voltage ratings

- • None = Standard production

• PbF = Lead (Pb)-free

A = D-61-8 package style

Standard pack quantity: A = 10 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95019					
Part marking information	http://www.vishay.com/doc?95030				
SPICE model http://www.vishay.com/doc?95290					

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